

In the Absratct

Please delete the Abstract of the Disclosure and associated materials found on Page 22.

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Please delete the Abstract of Disclosure found on Page 23 and replace this Abstract with the following Abstract of Disclosure.

10 - -In a data processing system ~~have~~ having a master-state data processing unit and at least one slave-state data processing unit, the data processing units can be provided with an asynchronous transfer mode interface unit **18** for transferring data cells there between. The
15 interface unit **18** provides and receives signals formatted in the Utopia protocol. The interface unit **18** includes a processor acting as a state machine **181, 184** and a buffer out memory unit **182, 183** for buffering the data groups between the interface unit processor and the direct
20 memory access unit **14** of the data processing unit. The interface unit **18** can act in a receive mode and a transmit mode for a master-state data processing unit and can act in a receive mode **181, 182** and transmit mode **184, 183** in a slave-state data processing unit. An event
25 signal provides an efficient exchange of transfer of data between the direct memory access unit **14** and the buffer memory storage unit **182, 183** in the slave mode.- -